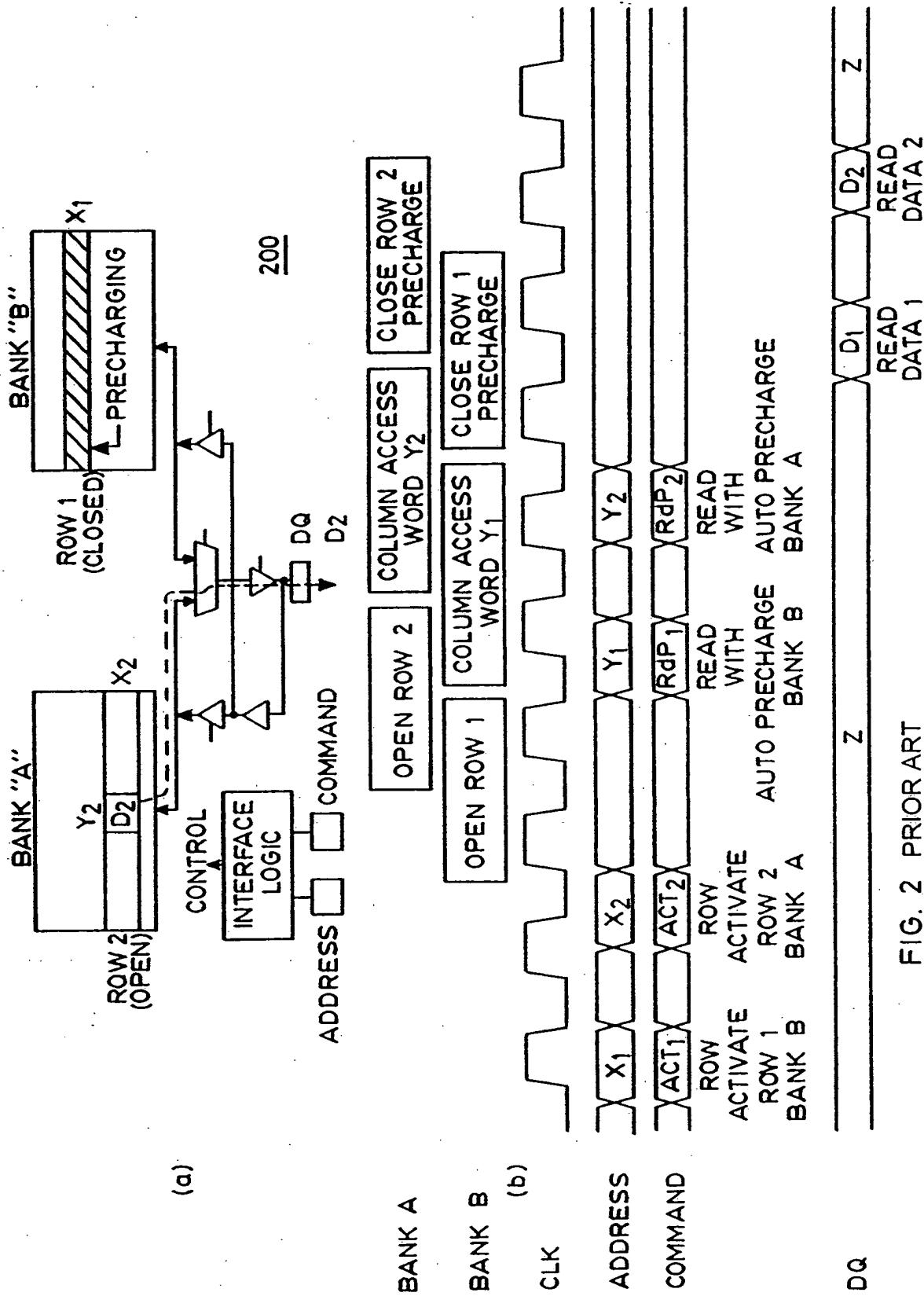


FIG. 1 PRIOR ART



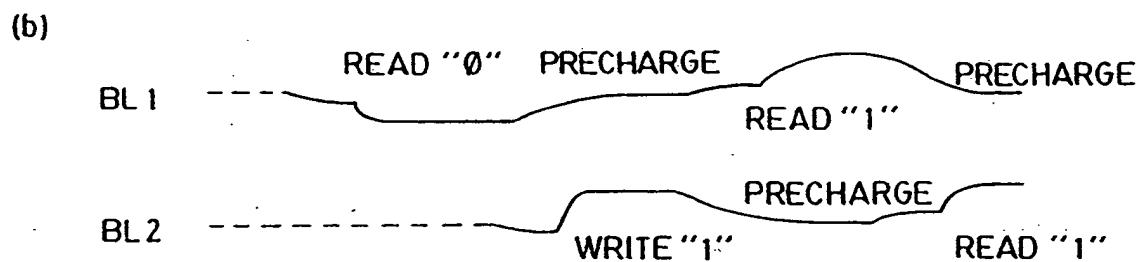
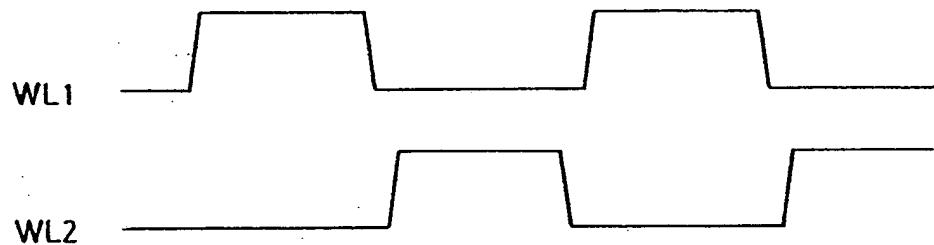
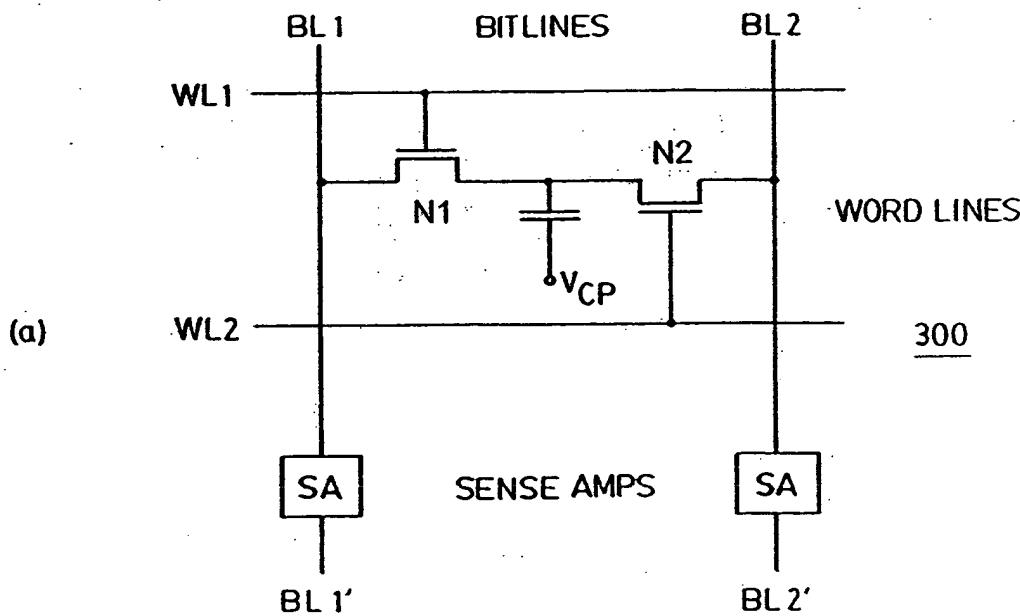


FIG. 3 PRIOR ART

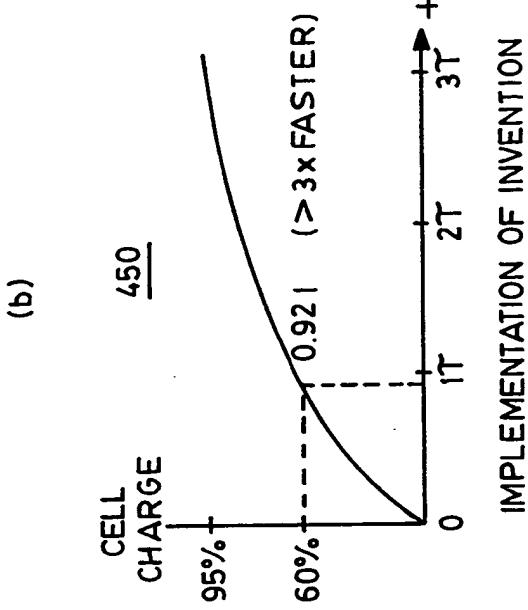
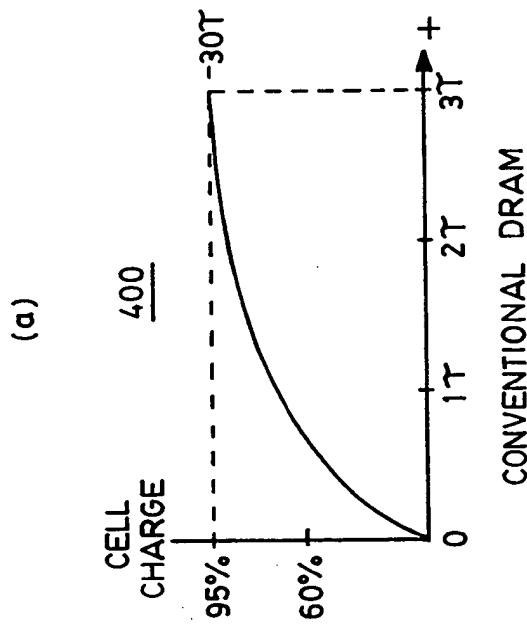
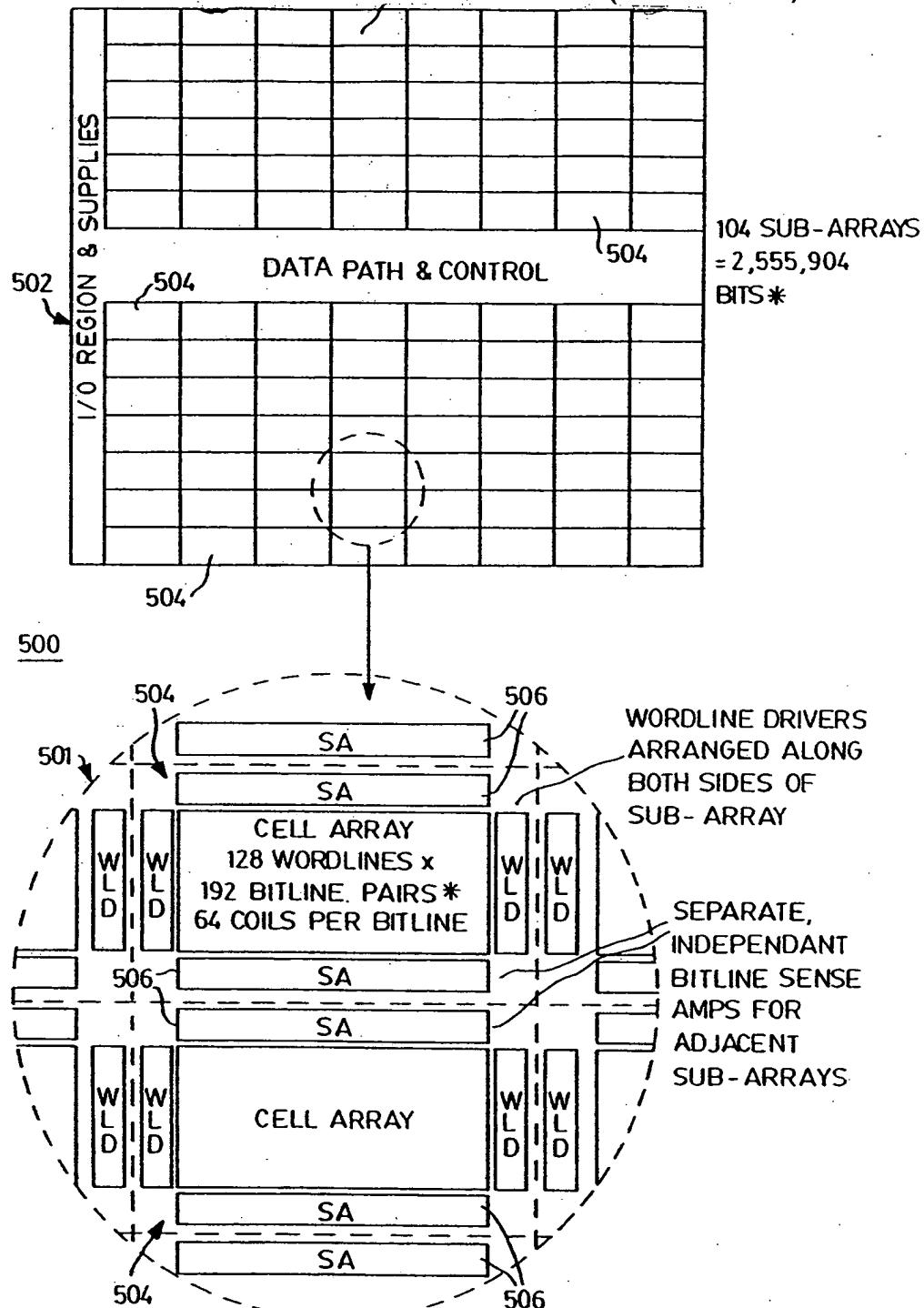


FIG. 4

Appl. No. 10/336,850
Amdt. Dated August 18, 2003
Reply to Office action of June 18, 2003
Replacement Sheet

MEMORY SUB-ARRAY (1024x 24 BITS)*



***REDUNDANT ELEMENTS
NOT INCLUDED**

FIG. 5

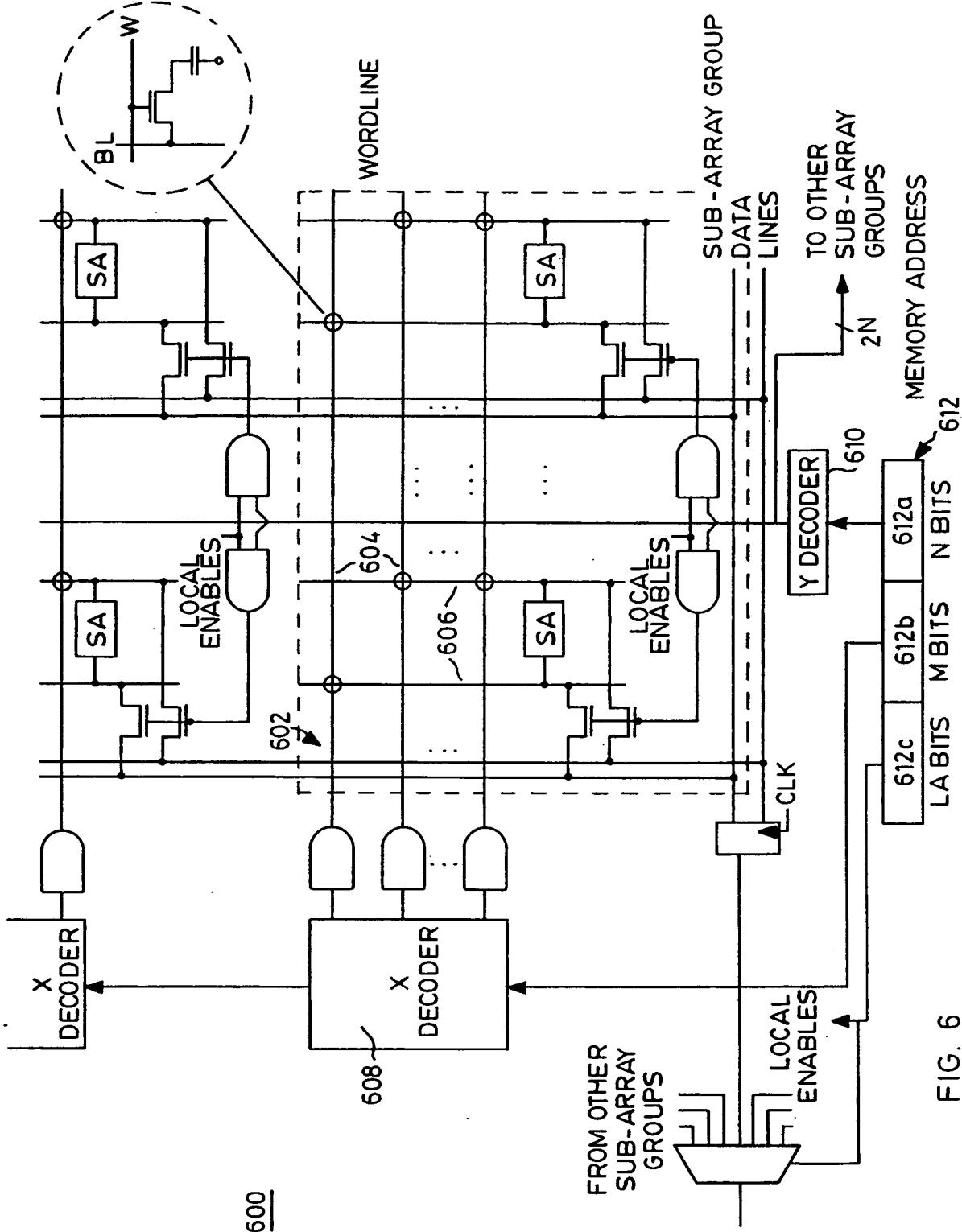


FIG. 6

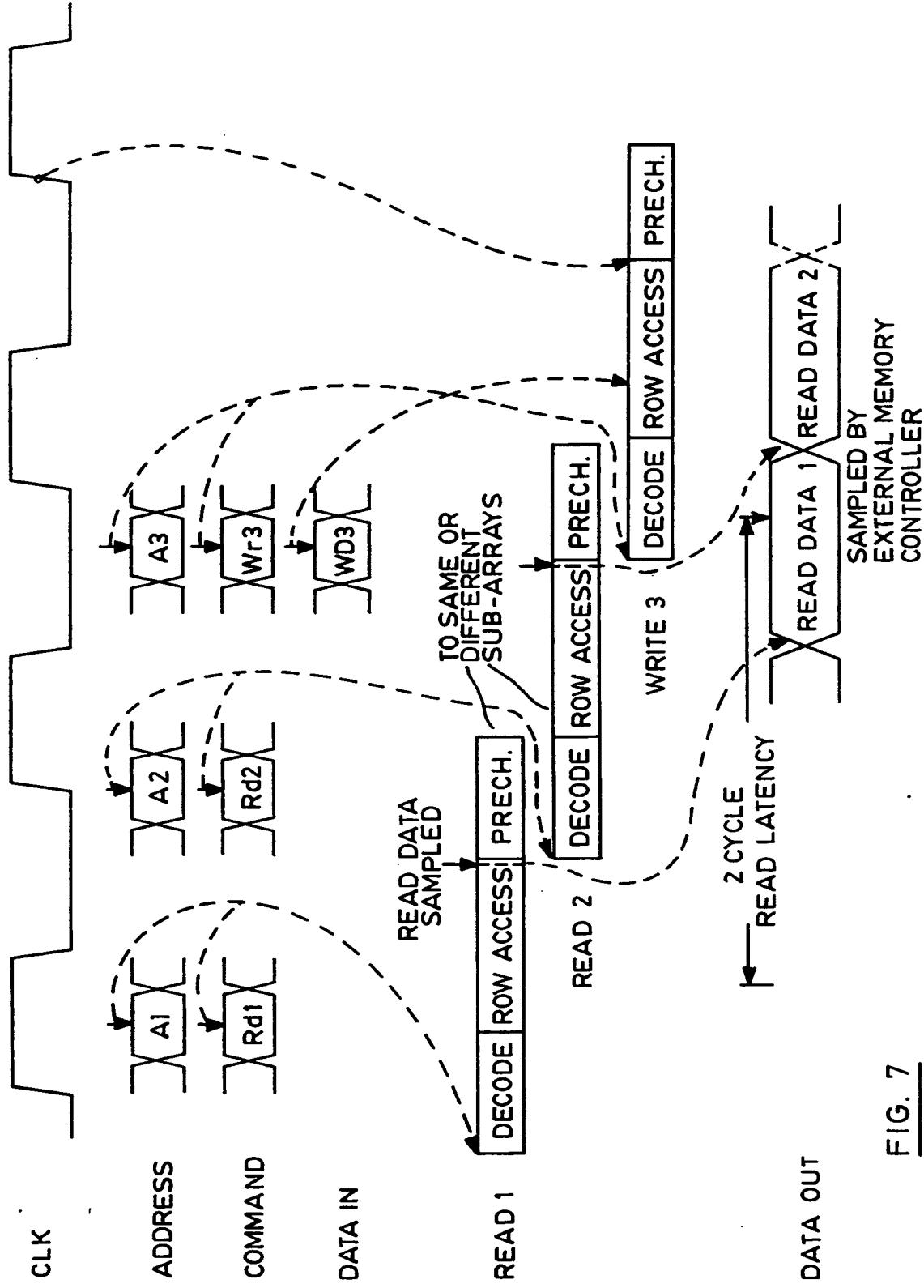
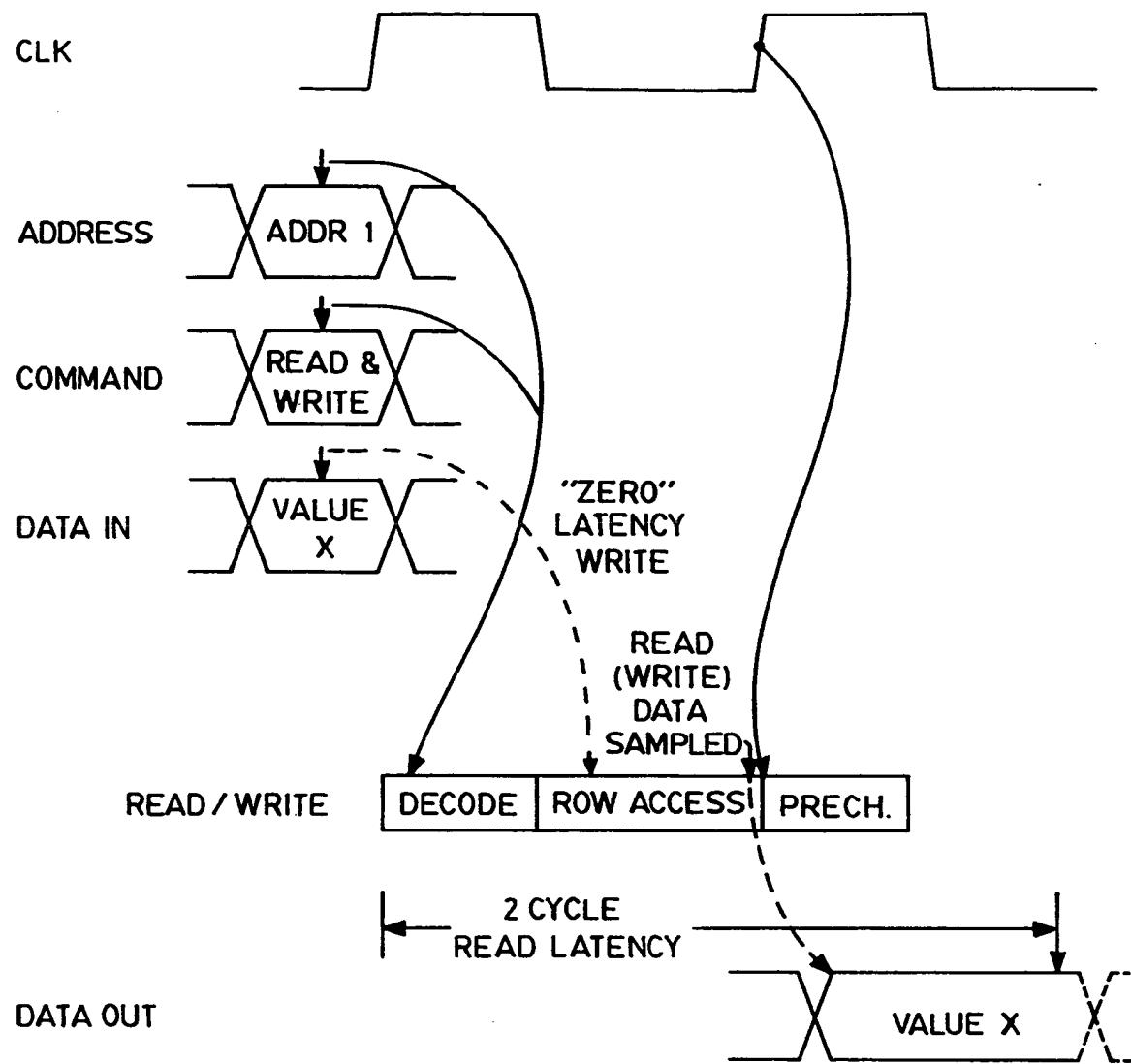


FIG. 7



800

FIG. 8

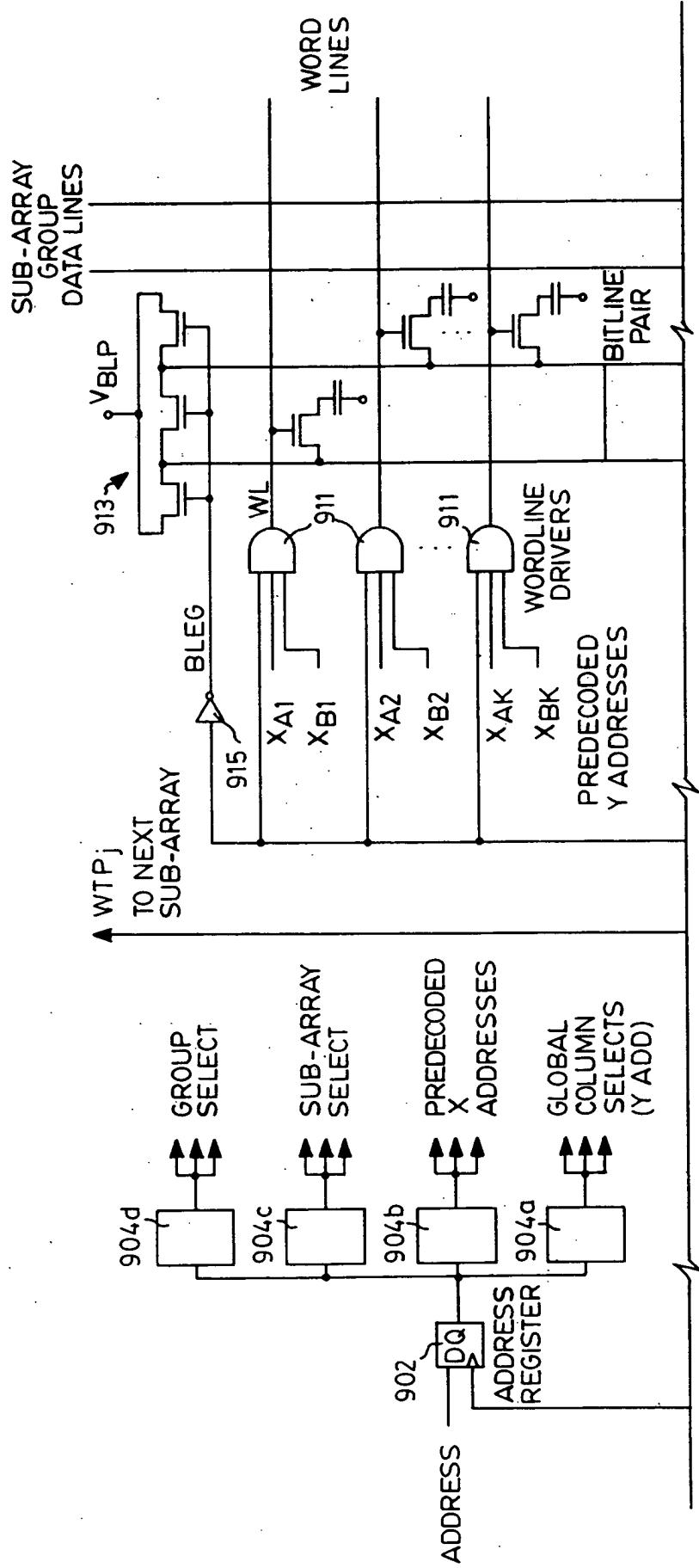


FIG. 9a

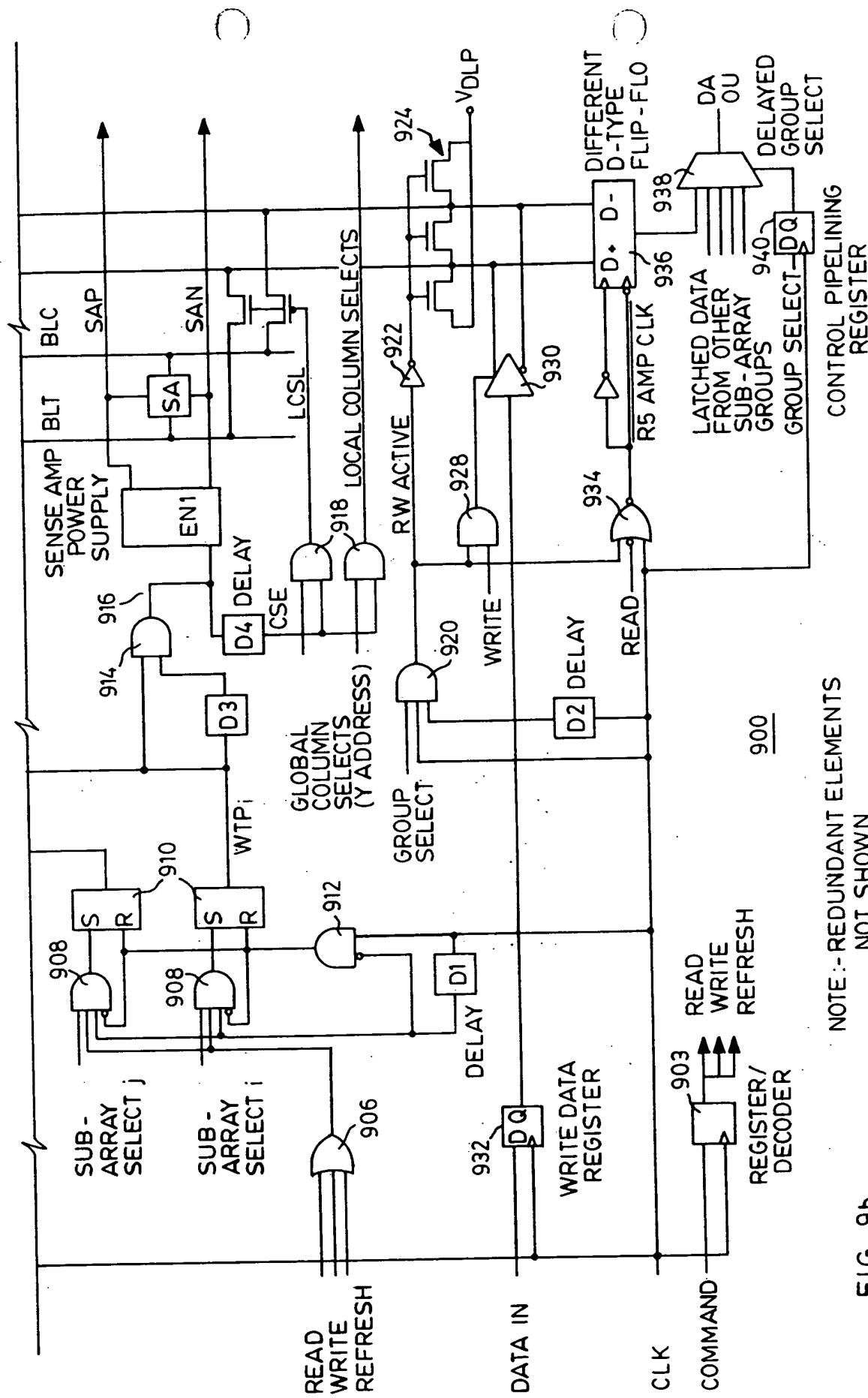


FIG. 9b

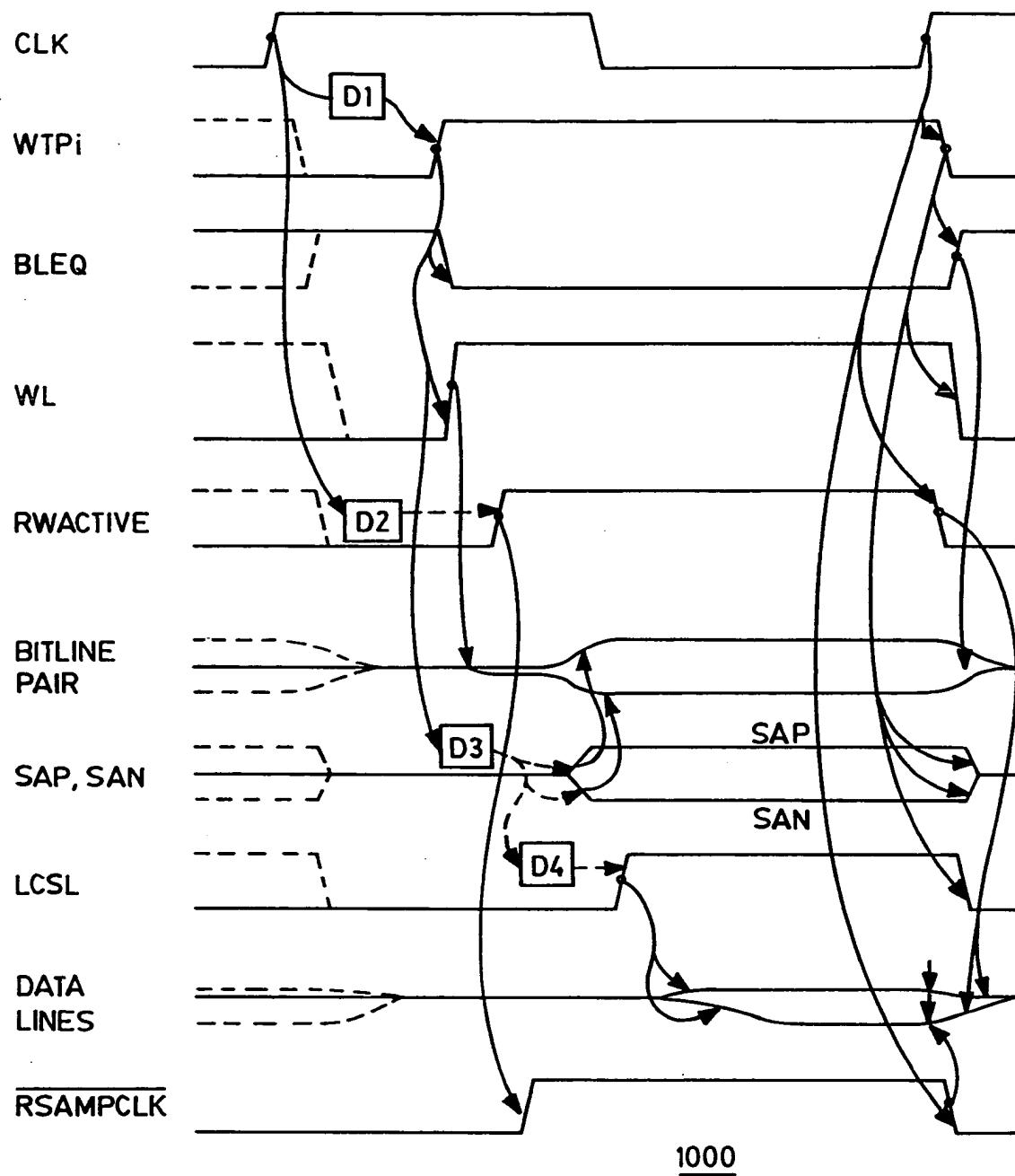


FIG. 10a

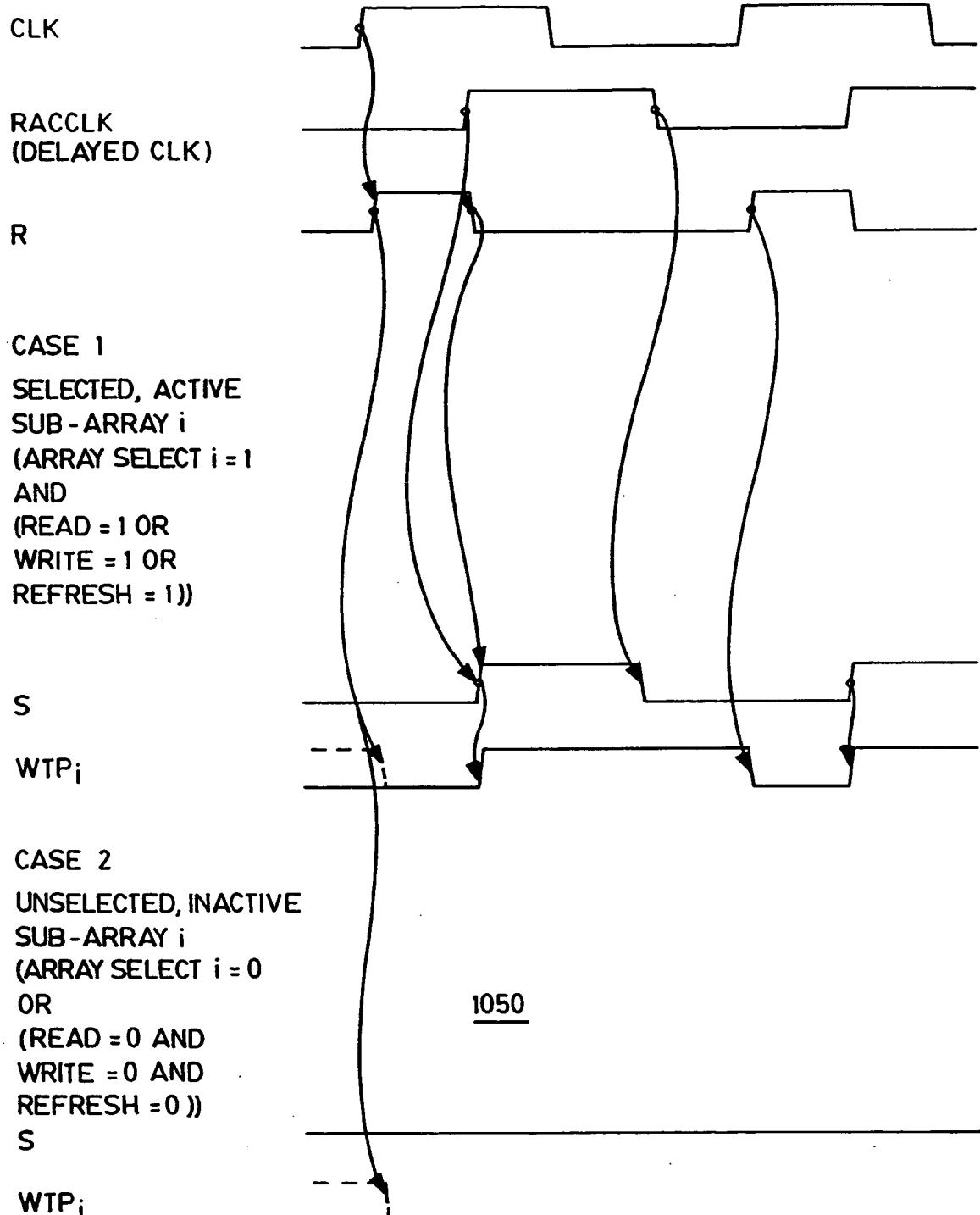


FIG. 10b

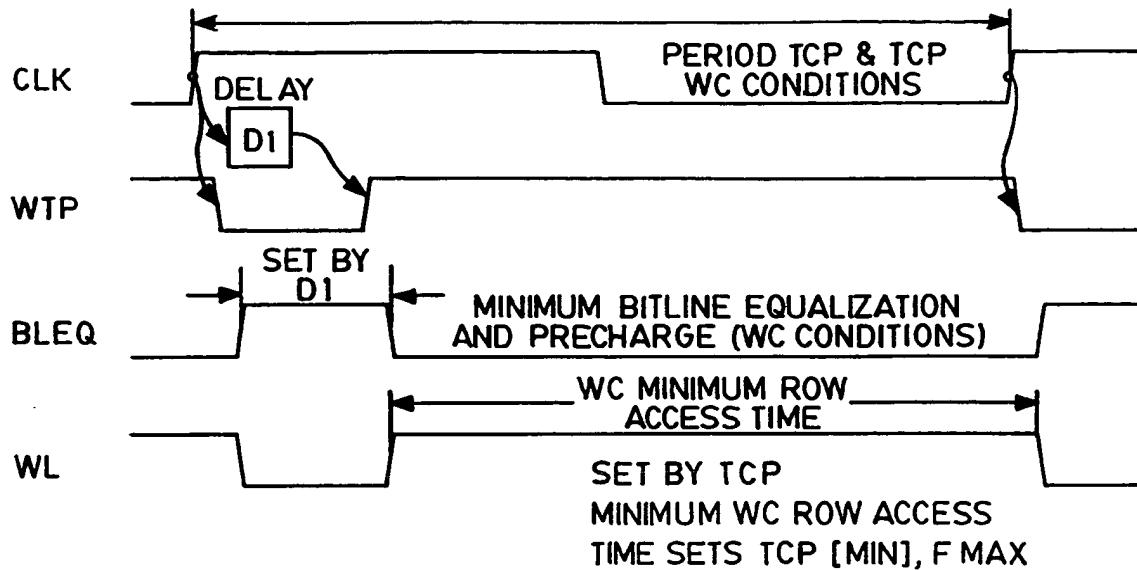


FIG. 11a

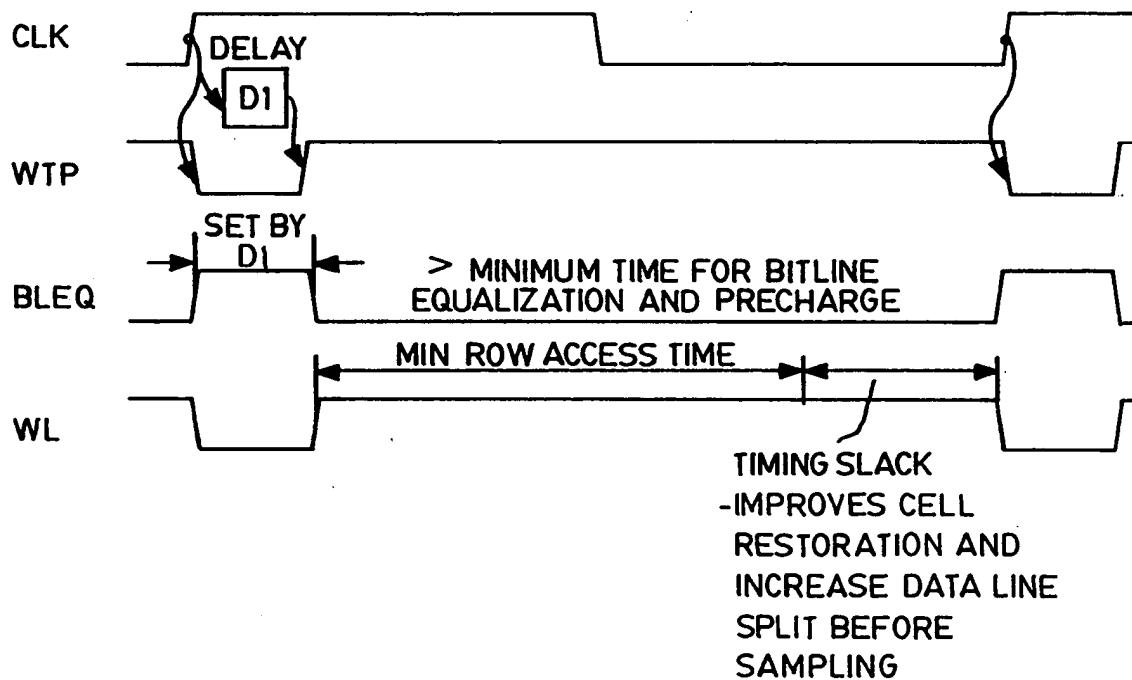


FIG. 11b

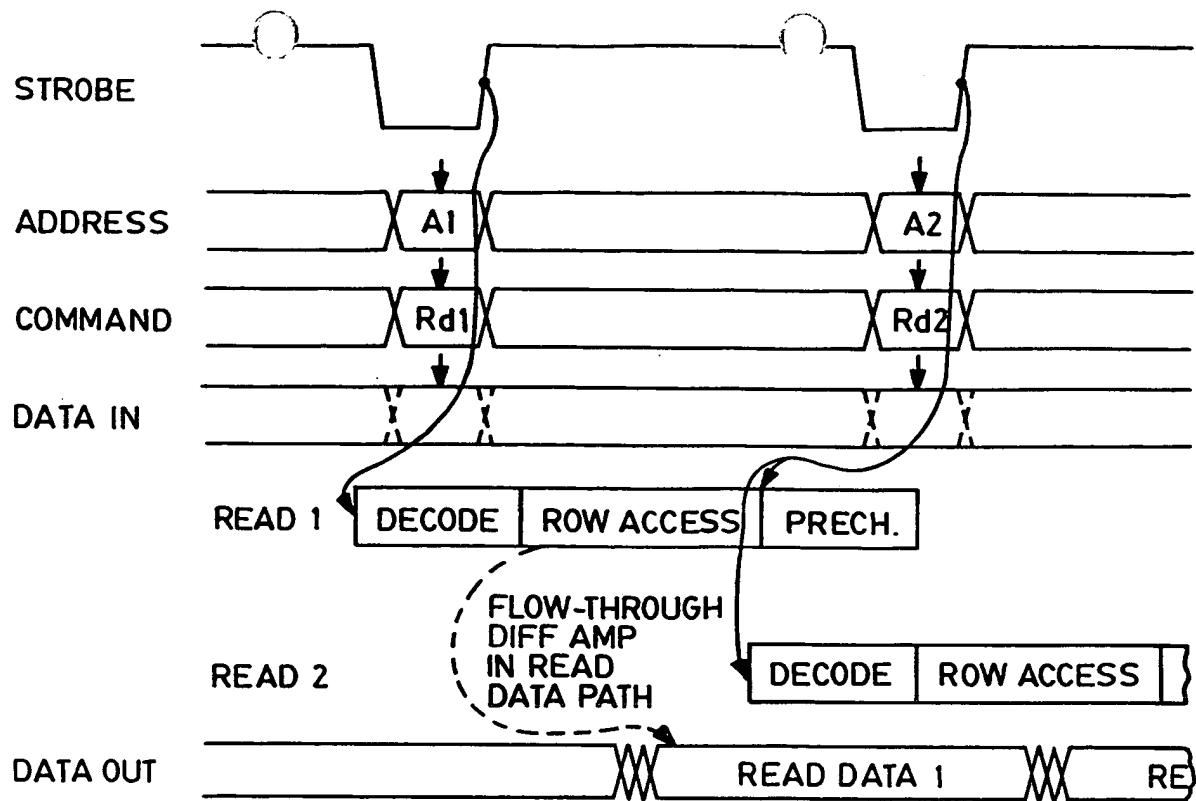


FIG. 12a

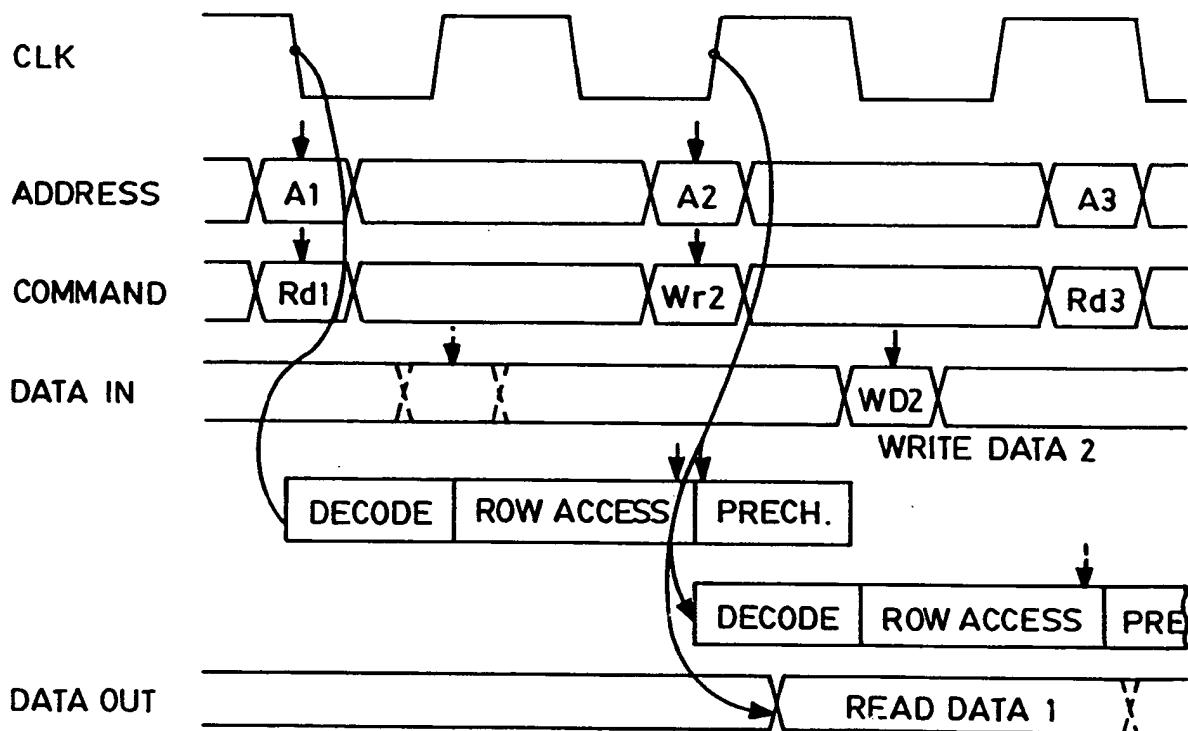


FIG. 12b

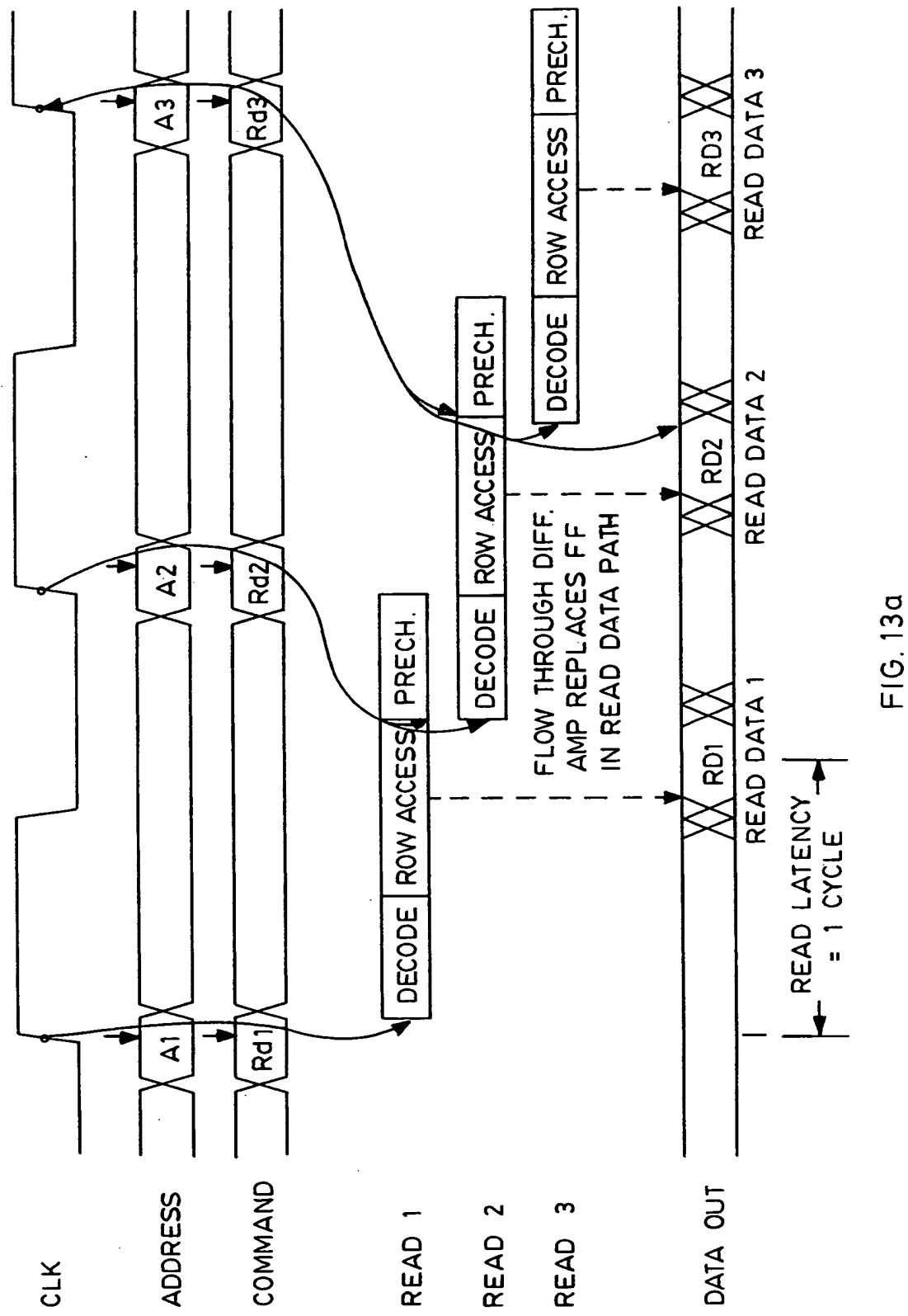


FIG. 13a

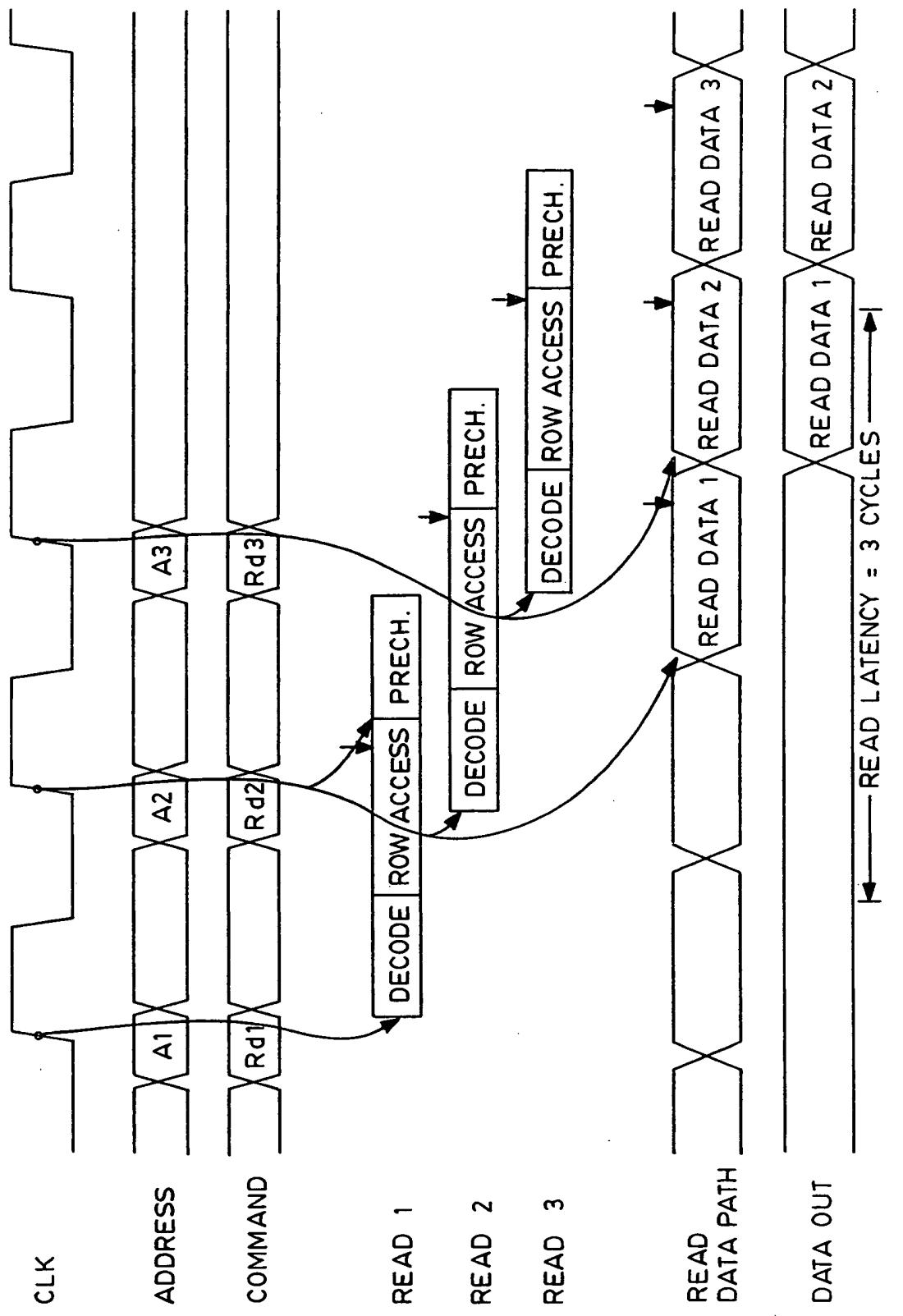


FIG. 13b